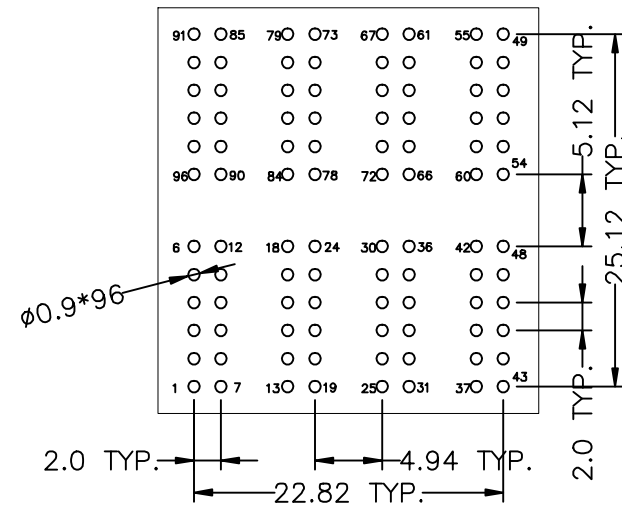
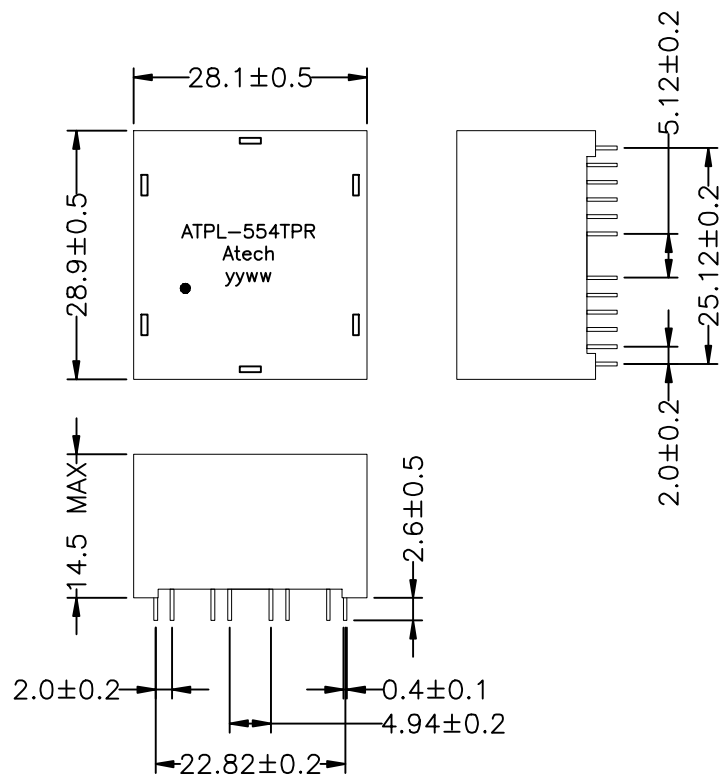




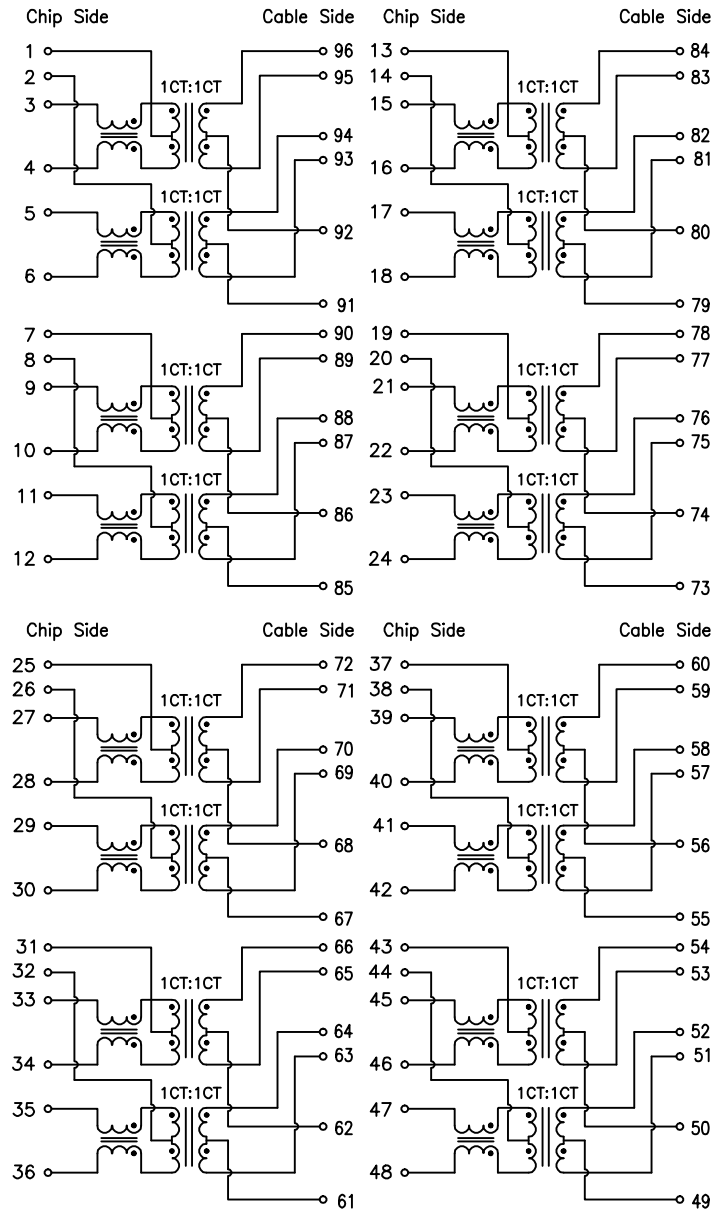
PCS LAYOUT MOUNTING HOLE (TOP VIEW)

ELECTRICAL CHARACTERISTICS @25°C :

1. INSULATION HI-POT(PRI./SEC.) : 1500Vrms, 1mA, 60SEC
2. TURN RATIO ($\pm 5\%$) : TX = 1CT : 1CT , RX = 1CT : 1CT
3. OCL : 350uH MIN. @100kHz, 0.1V WITH 21mA DC BIAS
4. LEAKAGE INDUCTANCE : 0.5uH MAX @100kHz, 0.1V
5. DCR : 1400m Ω MAX.
6. INSERTION LOSS : -1.2dB MAX.(@0.5-1MHz)
-0.2-0.002*f^{1.4}dB MAX (@1-125MHz)
7. RETURN LOSS : -16dB MIN.(@1-40MHz)
-10+20*log (f/80) dB MIN (@40.1-100MHz)
8. CROSS TALK : 1-100MHz : -30dB MIN.
9. CMRR : 1-100MHz : -30dB MIN.
10. OPERATING TEMPERATURE RANGE : -40°C~+85°C
11. PoE Support 90W : Each channel support 700mA DC balance current,
One gigabit Base-T(Full channels) port support 1400mA DC balance current.
12. STORAGE TEMPERATURE : -40°C to +85°C
13. RoHS COMPLIANT

UNLESS OTHERWISE SPECIFIED ALL TOLERANCES ARE XX.XX $\pm$ 0.25 0.XX $\pm$ 0.05

								TITLE 100/1000 BASE-T PoE++ TRANSFORMER	
								DWG. NO. ATPL-554TPR-10	
	RELEASE	05/31/2021	SHIRLEY	BETTY	BETTY	UNITS: M/M	SAFETY		SHEET 1 of 2
NO:	DESCRIPTION	DATE	BY	CHK	APPD	DATE	P/N:	DRAW	
REVISIONS						05/31/21			SHIRLEY



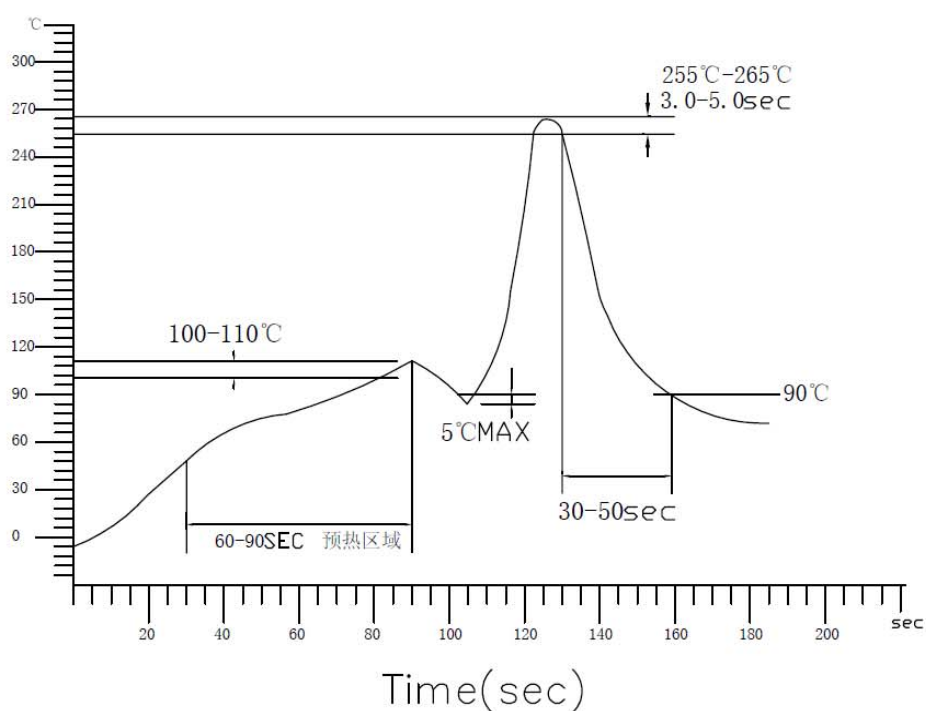
							TITLE 100/1000 BASE-T PoE++ TRANSFORMER	
							DWG. NO. ATPL-554TPR-10	
	RELEASE	05/31/2021	SHIRLEY	BETTY	BETTY	UNITS: M/M	SAFETY	
NO:	DESCRIPTION	DATE	BY	CHK	APPD	DATE	P/N:	DRAW
REVISIONS						05/31/21	SHEET 1 of 2	
							SHIRLEY	

6. Soldering Heat Withstanding Survey (DIP)

Pb-free Soldering Heat Withstanding Survey(DIP)

Reflow conditions	Preheat area	Time at 220℃ or higher	Peak Temp	Holding time at 260℃ temp
Recommended conditions	Max 120s	Max 30s	260℃	Max 6S

Soldering Temp.profile



Soldering conditions	Soldering Temp.	Heating Time
Recommended conditions	260℃±5℃	Max 6 sec

Soldering Iron Conditions	Soldering Iron Temp	Heating Time
Recommended conditions	350±10℃	Max 5 sec

Part Electrode Plating Constitution	Sn.CU
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Any other series applicable same conditions	NO
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Packing Configuration	TRAY
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