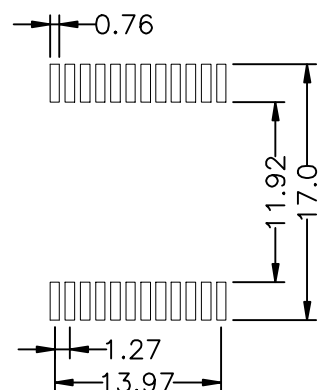
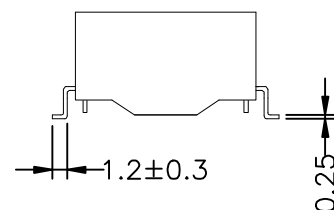
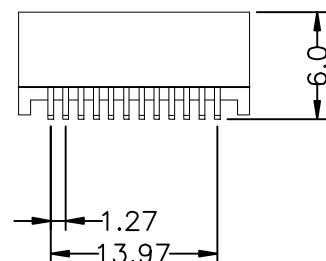
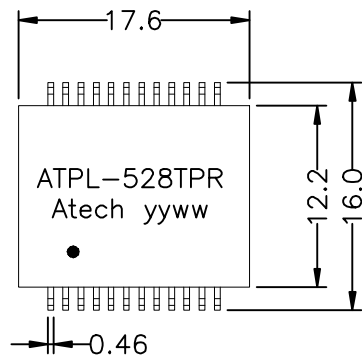
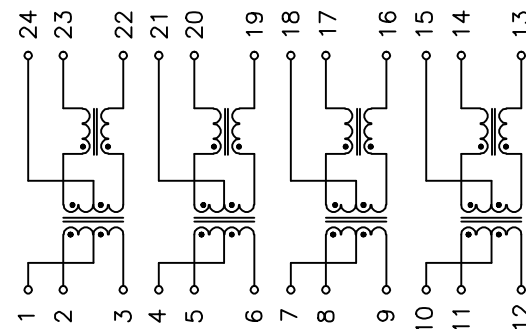




SUGGESTED PCB LAYOUT

UNLESS OTHERWISE SPECIFIED ALL TOLERANCES ARE $\pm 0.25\text{mm}$

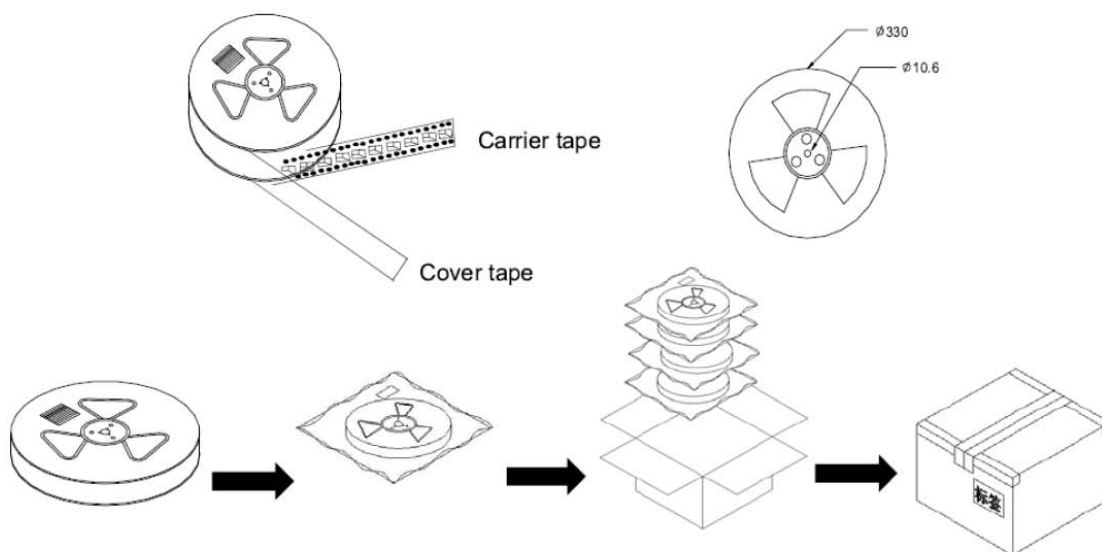
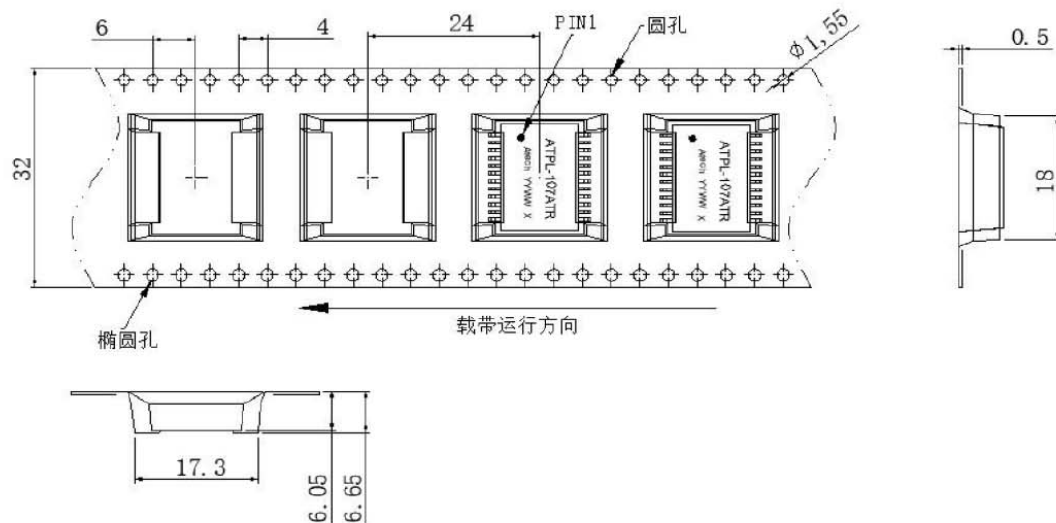


ELECTRICAL CHARACTERISTICS @25°C :

1. INSULATION HI-POT(PRI./SEC.) : 1500Vrms, 1mA, 60SEC
2. TURN RATIO ($\pm 5\%$) : TX = 1CT : 1CT , RX = 1CT : 1CT
3. OCL : 350uH MIN. @100kHz, 0.1V WITH 8mA DC BIAS
4. LEAKAGE INDUCTANCE : 0.5uH MAX @100kHz, 0.1V
5. DCR : 1.2 Ω MAX.
6. INSERTION LOSS (dB MAX.) : -1.0dB MAX.(@1-100MHz)
7. RETURN LOSS (dB MIN.) : 1-30MHz 40MHz 50MHz 60-80MHz 100MHz
-18 -14.4 -13.1 -12 -10
8. CROSS TALK (dB MIN.) : 1-100MHz : -30dB MIN.
9. CMRR (dB MIN.) : 1-100MHz : -30dB MIN.
10. OPERATING TEMPERATURE RANGE : -40°C~+85°C
11. IEEE802.3at COMPLIANT PERFORMANCE---720mA Current capability
12. RoHS COMPLIANT

								TITLE		10/100/1000 BASE-T PoE+ TRANSFORMER	
								DWG. NO.		ATPL-528TPR	
	RELEASE	03/08/2019	SHIRLEY	BETTY	BETTY	UNITS:	M/M	SAFETY			
NO:	DESCRIPTION	DATE	BY	CHK	APPD	DATE	03/08/19	P/N:		DRAW	
REVISIONS								SHEET 1 OF 1			
								DRAW		SHIRLEY	

3. Package:



Product	PCS/Reel	PCS/Box	Product No.	Weight(g/pcs)
SMD 24 PIN Series	400	400x6=2400		1.45

Remarks:

- 1) Packing Level & Storage Condition based on MSL1
- 2) ESD package
- 3) Parts packaging meet EIA-481 standard (pull 0.26N~1.3N)

Pb-free Soldering Heat Withstanding Survey

