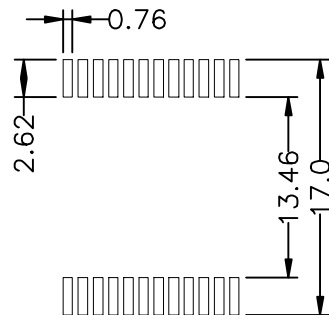
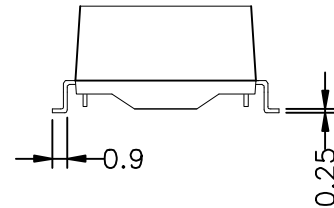
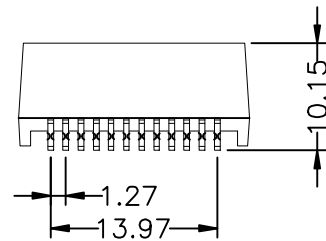
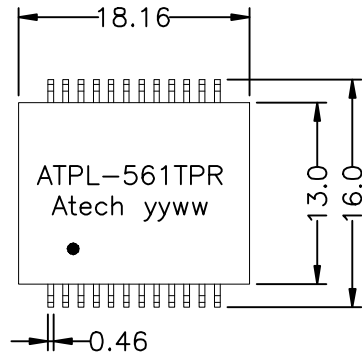
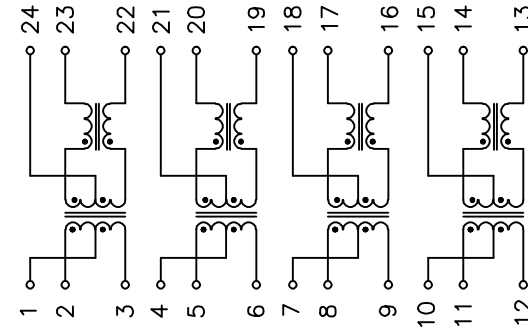




SUGGESTED PCB LAYOUT

UNLESS OTHERWISE SPECIFIED ALL TOLERANCES ARE $\pm 0.25\text{mm}$



ELECTRICAL CHARACTERISTICS @25°C :

1. INSULATION HI-POT(PRI./SEC.) : 1500Vrms, 1mA, 60SEC
2. TURN RATIO ($\pm 5\%$) : TX = 1CT : 1CT , RX = 1CT : 1CT
3. OCL : 350uH MIN. @100kHz, 0.1V WITH 36mA DC BIAS
4. LEAKAGE INDUCTANCE : 0.5uH MAX @100kHz, 0.2V
5. C_{ww} : 35pF MAX@100kHz, 0.2V
6. DCR : 1.2 Ω MAX.
7. INSERTION LOSS (dB MAX.) : -1.0dB MAX.(@1-100MHz)
8. RETURN LOSS (dB MIN.) : 1-30MHz 40MHz 60-80MHz 80-100MHz
-18 -14.4 -12 -10
9. CROSS TALK (dB MIN.) : 30MHz 60MHz 100MHz
-45 -40 -35
10. CMRR (dB MIN.) : 30MHz 60MHz 100MHz
-43 -37 -33
11. STORAGE TEMPERATURE RANGE : -40°C~+85°C
12. OPERATING TEMPERATURE RANGE : -40°C~+85°C
13. IEEE802.3at COMPLIANT PERFORMANCE---1.2A Current capability
14. RoHS COMPLIANT

								TITLE 10/100/1000 BASE-T PoE+ TRANSFORMER	
								DWG. NO. ATPL-561TPR	
	RELEASE	08/17/2022	SHIRLEY	BETTY	BETTY	UNITS: M/M	SAFETY		SHEET 1 of 1
NO:	DESCRIPTION	DATE	BY	CHK	APPD	DATE 08/17/22	P/N:		DRAW SHIRLEY
REVISIONS									

Pb-free Soldering Heat Withstanding Survey

