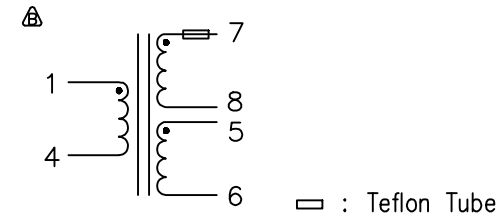
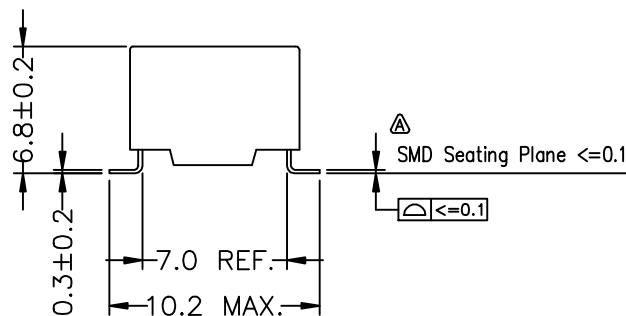


SUGGESTED PCB LAYOUT

UNIT:mm



ELECTRICAL CHARACTERISTICS :

- DCR : PIN 1-4 = 32mΩ MAX.
PIN 8-7 = 42mΩ MAX.
PIN 6-5 = 13mΩ MAX.

- INDUCTANCE (@2MHz, 0.1Vrms)
PIN 1-4 = 9.8uH ±25%
PIN 5-6 = 1.1uH ±25%
PIN 7-8 = 17.5uH ±25%

- TURN RATIO (@100KHz, 0.1Vrms)
PIN 1-4 : 5-6 = 3 ±2%
PIN 7-8 : 5-6 = 4 ±2%

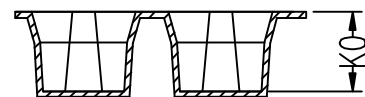
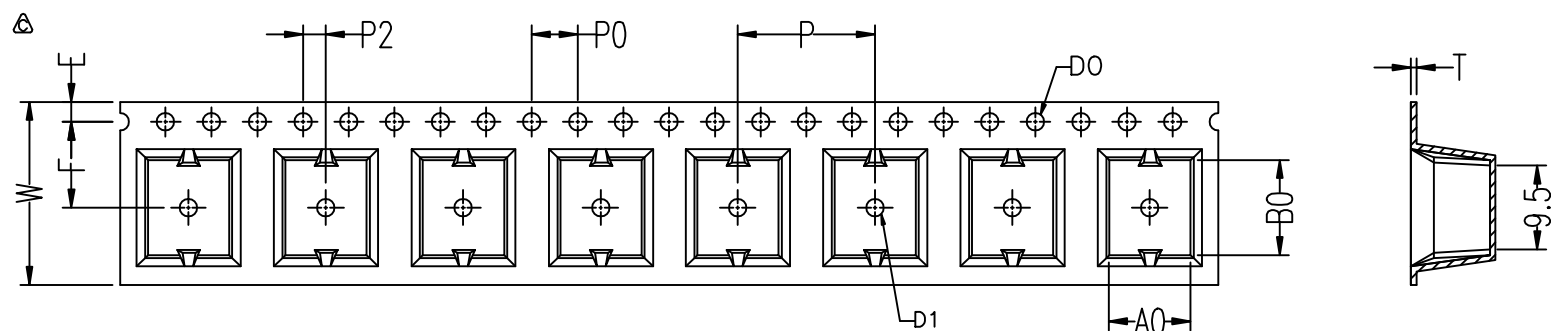
- HI-POT : PIN 1 - PIN 7.5 (@3000VAC, 2mA, 1SEC)

- OPERATING TEMPERATURE : -40°C~+85°C

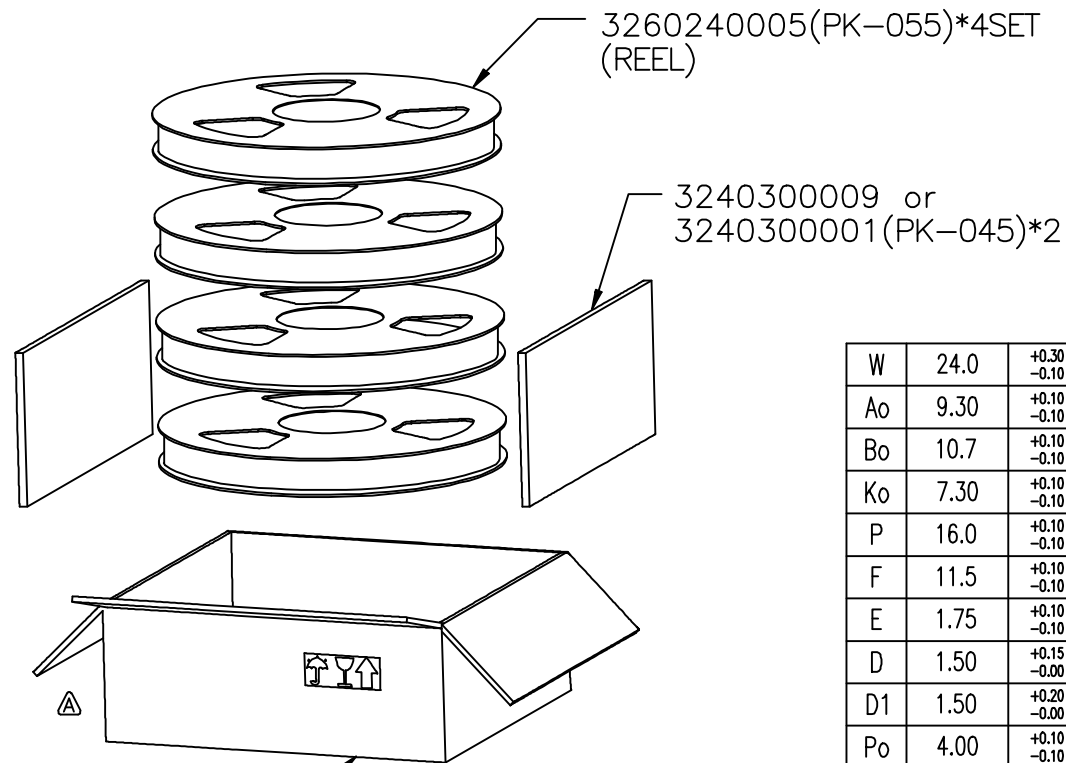
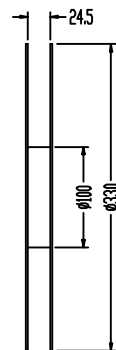
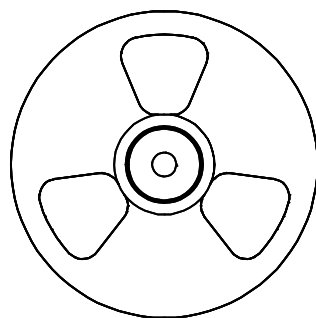
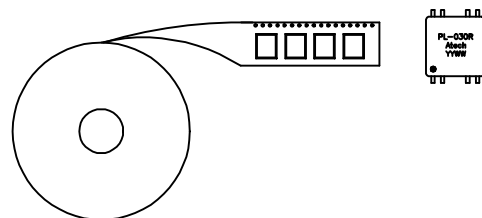
- STORAGE TEMPERATURE : 0°C~+70°C

- RoHS COMPLIANT

C	CHANGE INDUCTANCE	08/03/2023	SHIRLEY	RONAN	RONAN		TITLE TRANSFORMER	
B	CHANGE CIRCUIT	09/19/2016	SHIRLEY	RONAN	RONAN		DWG. NO. PL-030R-GF	SHEET 1 of 1
A	ADD TEMPERATURE RANGE	07/06/2016	SHIRLEY	RONAN	RONAN			
	RELEASE	07/24/2013	SHIRLEY	RONAN	RONAN			
NO:	DESCRIPTION	DATE	BY	CHK	APPD	UNITS: M/M	SAFETY	DRAW SHIRLEY
REVISIONS						07/24/13		



DIRECTION OF FEED



3260240005(PK-055)*4SET
(REEL)

3240300009 or
3240300001(PK-045)*2

W	24.0	+0.30 -0.10
Ao	9.30	+0.10 -0.10
Bo	10.7	+0.10 -0.10
Ko	7.30	+0.10 -0.10
P	16.0	+0.10 -0.10
F	11.5	+0.10 -0.10
E	1.75	+0.10 -0.10
D	1.50	+0.15 -0.00
D1	1.50	+0.20 -0.00
Po	4.00	+0.10 -0.10
P2	2.00	+0.10 -0.10
T	0.40	+0.05 -0.05

3213434003(PK-071A)*1PCS
(CARTON)

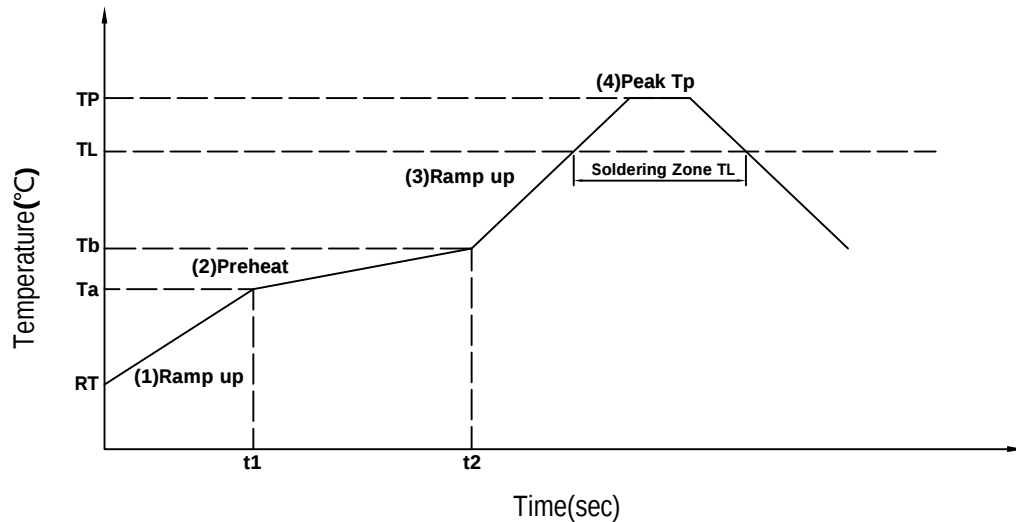
ONE REEL 500 PCS
ONE CARTON 4 REEL (2000PCS)

每卷放乾燥包1PCS 用真空袋抽真空封口
卷軸的尺寸是符合EIA-481-C的標準 (拉力0.1N~1.3N)

C	ADD TRAY DRAWING	06/06/2018	SHIRLEY	JOAN	JOAN		TITLE		PACKING
B	ADD NOTE	07/12/2016	SHIRLEY	JOAN	JOAN		DWG. NO.		968PL03000
A	DEL LOGO	05/17/2013	SHIRLEY	JOAN	JOAN		UNITS:		M/M
	RELEASE	11/07/2012	SHIRLEY	JOAN	JOAN		SAFETY		
NO:	DESCRIPTION	DATE	BY	CHK	APPD	DATE	P/N:		
REVISIONS						11/07/12	SHEET 1 of 1		
							DRAW		Shirley



Pb-free Soldering IR Reflow(SMD)



- 1,MSL Grade: 1 Level 2,Floor life: 2years 3,Condition:≤30°C RH 85%
2,Form-1(Reference JEDEC J-STD-020D Table 5-2)

IR reflow profile		Pb-free
step#	Profile Feature	Condition/Duration
step1	Ramp-up rate	3°C/second max
step2	Preheat:150°C-200°C(Ta-Tb)	t1-t2:60-120seconds
step3	Ramp-up rate(TL to Tp)	3°C/second max
	Temperature maintained above 217°C	60-150seconds
step4	Peak temperature(Tp)	260+0/-5°C
	Time within 5°C of actual peak temperature	30seconds max
Ramp-down rate(Tp to TL)		6°C/second max

- 3,Form-2(Reference JEDEC J-STD-020D Table 4-2)

Package Thickness	Volume mm ³ <350	Volume mm ³ 350-2000	Volume mm ³ >2000
<1.6mm	260+0/-5°C	260+0/-5°C	260+0/-5°C
1.6mm-2.5mm	260+0/-5°C	250+0/-5°C	245+0/-5°C
>2.5mm	250+0/-5°C	245+0/-5°C	245+0/-5°C