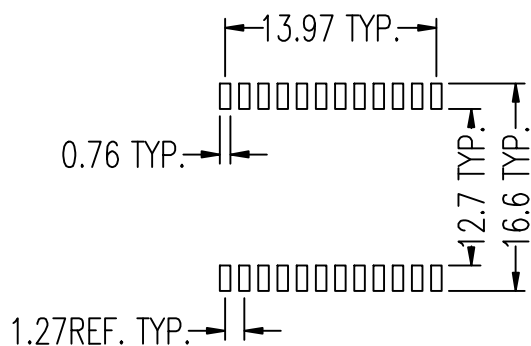
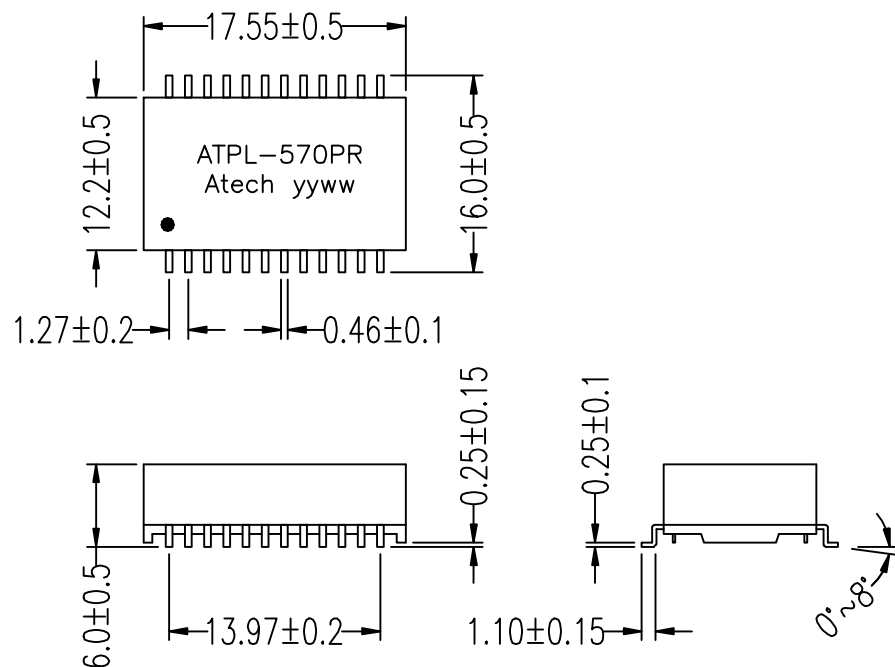
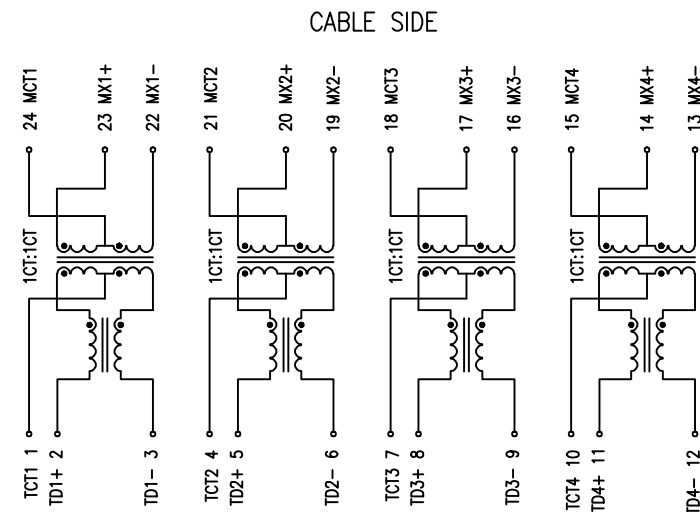




SUGGESTED PAD LAYOUT



CHIP SIDE

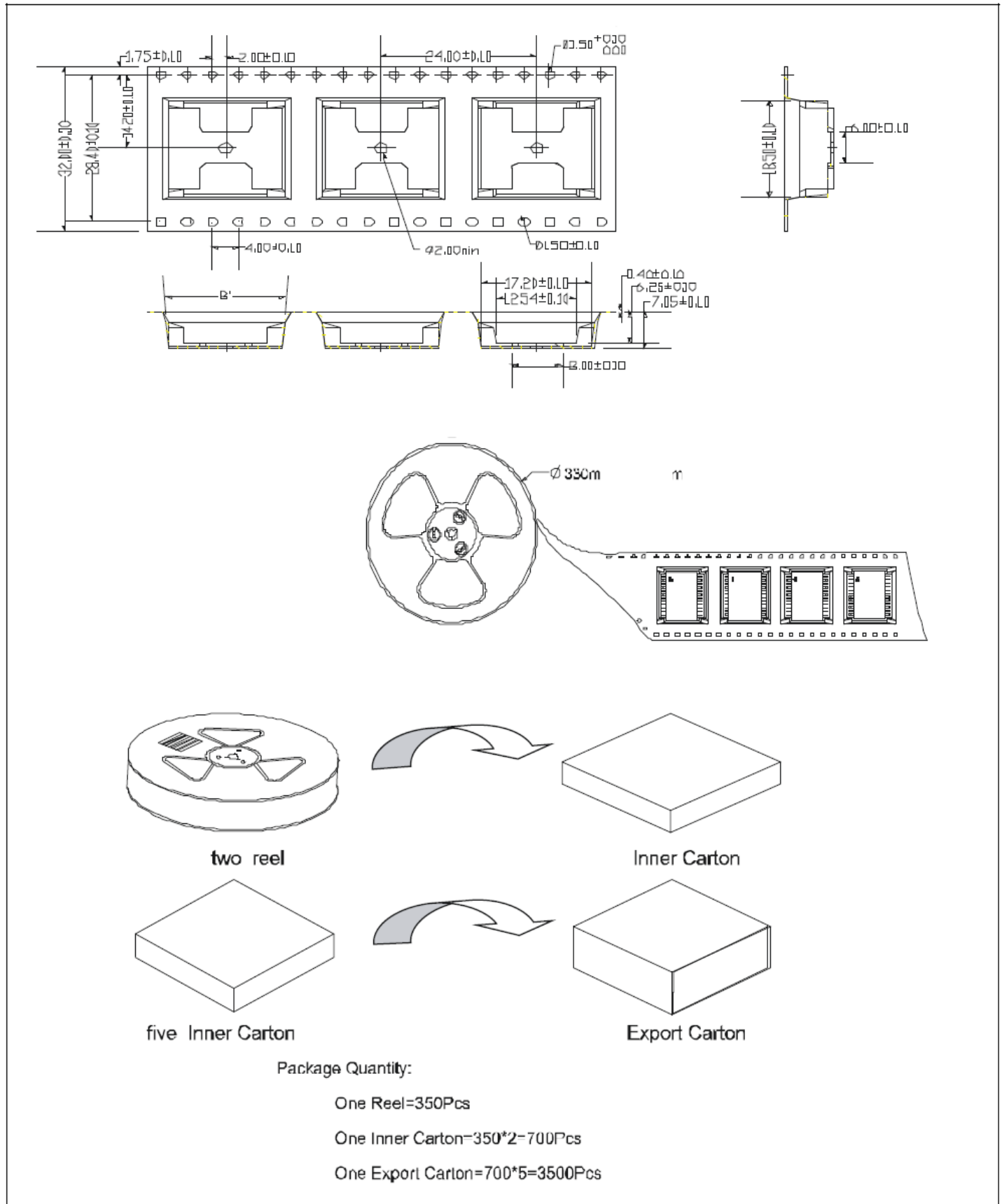
* RoHS COMPLIANT

ELECTRICAL CHARACTERISTICS @25°C:

- INSULATION HI-POT : 1500 Vrms/1mA ,60Sec.
- TURN RATIO (±5%) : 1CT : 1CT
- OCL : (@100kHz, 0.1Vrms) : 120uH MIN
- CROSS TALK : 1-500MHz : -25dB TYP.
- INSERTION LOSS : @1-200MHz : -1.2dB MAX. @200-300MHz : -1.5dB MAX.
@300-400MHz : -2.0dB MAX. @400-500MHz : -3.0dB MAX.
- RETURN LOSS : @1-100MHz : -18dB MIN. @100-200MHz : -16dB MIN.
@200-300MHz : -12dB MIN. @300-400MHz : -10dB MIN.
@400-500MHz : -8dB MIN.
- DIFFERENTIAL TO COMMON MODE REJECTION (dB MIN.) : 1-500MHz : -25dB TYP.
- LK(100kHz, 0.1Vrms) : 0.5uH MAX.
- DCR : 0.9Ω TYP.
- IEEE802.3bt compliant
- PoE SUPPORT 30W : Each channel support 720mA DC balance current,
- Operating temperature : 0°C to +70°C
Storage temperature : -40°C to +85°C

						AOEM_{Technologies}		TITLE 10G BASE-T	
								PoE++ TRANSFORMER	
								DWG. NO.	
								ATPL-570PR	
	RELEASE	01/13/2023	SHIRLEY	BETTY	BETTY	UNITS:	SAFETY		SHEET 1 OF 1
NO:	DESCRIPTION	DATE	BY	CHK	APPD	M/M			
						DATE	P/N:		DRAW
REVISIONS						01/13/23			SHIRLEY

3. Package :



Pb-free Soldering Heat Withstanding Survey

